

**IN THE SPECIFICATION:**

Please delete the paragraphs below and replace them with the paragraphs set forth immediately below in clean form. Additionally, in accordance with 37 CFR 1.121(b)(iii), all paragraphs amended herein are set forth in a marked up version on the sheets attached to this amendment.

**Last Paragraph On Page 26:**

C1  
On the other hand, a second differential circuit 36A is formed by four diode-connected transistors Q36 to Q39 connected in parallel with each other and a ~~diode-connected~~ transistor Q40 having an emitter electrode connected to each of emitter electrodes of the transistors Q36 to Q39. A current source 42A is connected between a common emitter connection point of the transistors Q36 to Q40 and the GND line 34.

**Last Paragraph On Page 30 spanning to Page 31:**

C2  
On the other hand, a second differential circuit 56A is formed by four diode-connected transistors Q56 to Q59 connected in parallel with each other and each having a base electrode connected to the circuit input terminal 51 and a ~~diode-connected~~ transistor Q60 having an emitter electrode connected to each of emitter electrodes of the transistors Q56 to Q59. A current source 62A is connected between a common emitter connection point of the transistors Q56 to Q60 and the GND line 54.